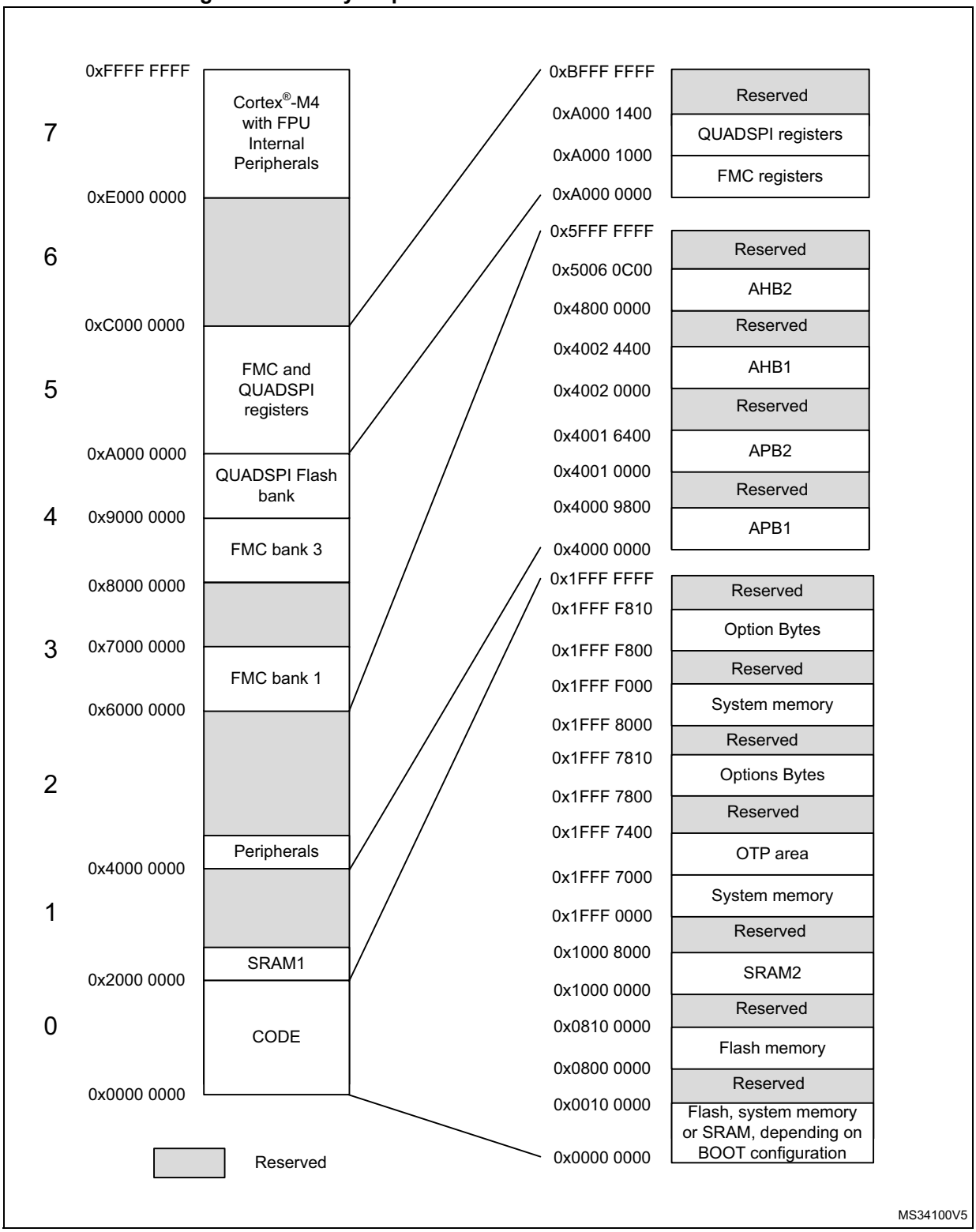Figure 3. Memory map for STM32L475xx/476xx/486xx devices